

# ACKNOWLEDGEMENT RECEIPT

Electronic Version

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## Title of Invention

SWITCHED CAPACITOR CIRCUIT CAPABLE OF MINIMIZING  
CLOCK FEEDTHROUGH EFFECT AND HAVING LOW PHASE  
NOISE AND METHOD THEREOF

Submission Type : Utility Patent Filing

Application Number:

10/710472



EFS ID:

64483

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ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
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First Named Applicant: En-Hsiang Yeh

Attorney Docket Number: MTKP0157USA4

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File Listing:

Doc. Name	File Name	Size (Bytes)	Date Produced (yyyymmdd)
us-power-of-attorney-grant	MTKP0157_4_A1-uspoat.xml	1067	2004-07-14
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us-power-of-attorney-grant	us-power-of-attorney-grant.xsl	12448	2004-07-14
us-ids	MTKP0157_4_A1-usidst.xml	812	2004-07-14
us-ids	us-ids.dtd	7763	2004-07-14
us-ids	us-ids.xsl	12026	2004-07-14
us-assignment	MTKP0157_4_A1-usassn.xml	1500	2004-07-14
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us-declaration	MTKP0157_4_A1-usdecl.xml	723	2004-07-14
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us-declaration	MTKP0157_4_A1DEC2.tif	28928	2004-07-14
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application-body	us-application-body.xsl	83497	2004-07-14

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